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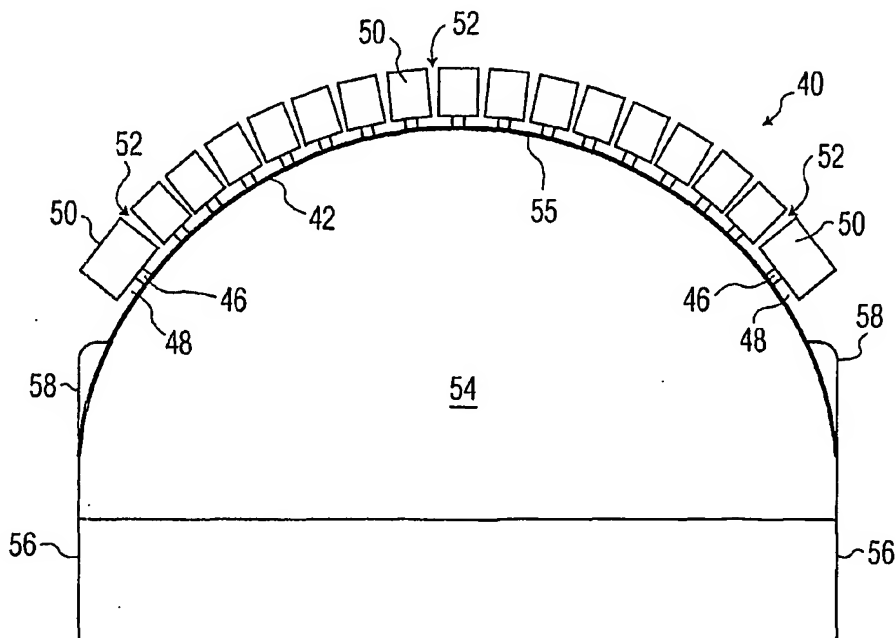
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(54) Title: **ULTRASOUND TRANSDUCER AND METHOD FOR IMPLEMENTING FLIP-CHIP TWO DIMENSIONAL ARRAY TECHNOLOGY TO CURVED ARRAYS**



(57) Abstract: An ultrasound transducer probe (40) includes a support substrate (54), an integrated circuit (42) and an array of piezoelectric elements (50). The support substrate (54) has a non-linear surface (55). The integrated circuit (42) physically couples to the support substrate (54) overlying the non-linear surface (55), wherein the integrated circuit (42) substantially conforms to a shape of the non-linear surface (55). An array of piezoelectric elements (50) couples to the integrated circuit (42).

**Declarations under Rule 4.17:**

- as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW, ARIPO patent (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU,

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- as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii)) for all designations

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